

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Appln. of: Josef Bock ET AL.

Appln. No.: Not yet assigned

Filed: Herewith

For: METHOD FOR FABRICATING A  
TRANSISTOR STRUCTURE

Attorney Docket No: 10808/235

Examiner: Not yet assigned

Art Unit: N/A

## INFORMATION DISCLOSURE STATEMENT

In accordance with the duty of disclosure under 37 C.F.R. §1.56 and §§1.97-1.98, and more particularly in accordance with 37 C.F.R. §1.97(b), Applicants hereby cite the following reference(s):

No.	Date of Publication	Patentee/Applicant/Assignee
US 2002/0079554 A1	June 27, 2002	Okawa et al.
US 2001/0045619 A1	November 29, 2001	Dekker et al.
4,882,294	November 21, 1999	Christenson
4,379,726	April 12, 1983	Kamamaru et al.
FOREIGN PATENT DOCUMENTS		
DOCUMENT NUMBER <small>Number-Kind Code (if known)</small>	DATE	COUNTRY
DE 100 44 838 A1	September 11, 2000	Germany
WO 97/17726	May 15, 1997	WO
JP 62154779	July 9, 1987	Japan
JP 58159346	September 21, 1983	Japan
JP 58009354	January 19, 1983	Japan
GB 1 480 050	July 20, 1977	Great Britain
DE 26 33 569	July 27, 1976	Germany
OTHER ART – NON PATENT LITERATURE DOCUMENTS		
<i>Bipolar Transistor with Pedestal Subcollector Regions Self-Aligned Underneath Field Oxide Regions</i> , pgs. 252-253, 700 IBM Technical Disclosure Bulletin, Vol. 31, No. 3, August 1988.		
M. Racanelli, K. Schuegraf, A. Kalburge, A. Kar-Ray, B. Shen, C. Hu, D. Chapek, D. Howard, D. Quon, F. Wang, G. U'ren, L. Lao, J. Zheng, J. Zhang, K. Bell, K. Yin, P. Joshi, S. Akhtar, S. Vo, T. Lee, W. Shi and P. Kempf, <i>Ultra High Speed SiGe NPN for Advanced BiCMOS Technology</i> , Silicon RF Platform Technologies, Conexant Systems, Inc.		
Copy of International Search Report from corresponding PCT patent application number PCT/DE03/03552.		
Copy of Examination Report from corresponding PCT patent application number PCT/DE03/03552.		

Applicants are enclosing Form PTO-1449 (one sheet), along with a copy of each listed reference for which a copy is required under 37 C.F.R. §1.98(a)(2). As

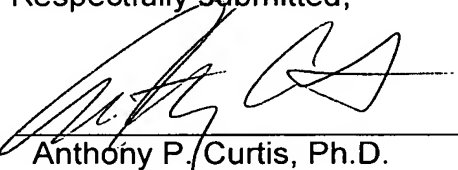
each of the listed references is in English, no further commentary is believed to be necessary, 37 C.F.R. §1.98(a)(3). Applicants respectfully request the Examiner's consideration of the above reference(s) and entry thereof into the record of this application.

By submitting this Statement, Applicants are attempting to fully comply with the duty of candor and good faith mandated by 37 C.F.R. §1.56. As such, this Statement is not intended to constitute an admission that any of the enclosed references, or other information referred to therein, constitutes "prior art" or is otherwise "material to patentability," as that phrase is defined in 37 C.F.R. §1.56(a).

Applicants have calculated no fee to be due in connection with the filing of this Statement. However, the Director is authorized to charge any fee deficiency associated with the filing of this Statement to a deposit account, as authorized in the Transmittal accompanying this Statement.

Respectfully submitted,

4/27/05  
Date

  
Anthony P. Curtis, Ph.D.  
(Reg. No. 46,193)

FORM PTO-1449	SERIAL NO. Not yet assigned	CASE NO. 10808/235
<b>LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT</b>	FILING DATE Herewith	GROUP ART UNIT N/A
(use several sheets if necessary)		APPLICANT(S): Josef Böck et al.

**REFERENCE DESIGNATION**

**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL	DOCUMENT NUMBER <small>Number-Kind Code (if known)</small>	DATE	NAME	CLASS/ SUBCLASS	FILING DATE
	A1	US 2002/0079554 A1	June 27, 2002	Okawa et al.	
	A2	US 2001/0045619 A1	November 29, 2001	Dekker et al.	
	A3	4,882,294	November 21, 1999	Christenson	
	A4	4,379,726	April 12, 1983	Kamamaru et al.	

**FOREIGN PATENT DOCUMENTS**

EXAMINER INITIAL	DOCUMENT NUMBER <small>Number-Kind Code (if known)</small>	DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES OR NO
	A5	DE 100 44 838 A1	September 11, 2000	Germany	
	A6	WO 97/17726	May 15, 1997	WO	
	A7	JP 62154779	July 9, 1987	Japan	
	A8	JP 58159346	September 21, 1983	Japan	
	A9	JP 58009354	January 19, 1983	Japan	
	A10	GB 1 480 050	July 20, 1977	Great Britain	
	A11	DE 26 33 569	July 27, 1976	Germany	

**OTHER ART – NON PATENT LITERATURE DOCUMENTS**

EXAMINER INITIAL	(Include name of author, title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date page(s), volume-issue number(s), publisher, city and/or country where published.
	A12 <i>Bipolar Transistor with Pedestal Subcollector Regions Self-Aligned Underneath Field Oxide Regions</i> , pgs. 252-253, 700 IBM Technical Disclosure Bulletin, Vol. 31, No. 3, August 1988.
	A13 M. Racanelli, K. Schuegraf, A. Kalburge, A. Kar-Ray, B. Shen, C. Hu, D. Chapek, D. Howard, D. Quon, F. Wang, G. U'ren, L. Lao, J. Zheng, J. Zhang, K. Bell, K. Yin, P. Joshi, S. Akhtar, S. Vo, T. Lee, W. Shi and P. Kempf, <i>Ultra High Speed SiGe NPN for Advanced BiCMOS Technology</i> , Silicon RF Platform Technologies, Conexant Systems, Inc.
	A14 Copy of International Search Report from corresponding PCT patent application number PCT/DE03/03552.
	A15 Copy of Examination Report from corresponding PCT patent application number PCT/DE03/03552.

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.